

S.S.H.
8-18-95



501.20699VC3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): R. HORI, et al.

Serial No.: 08/371,973

Filed: January 12, 1995

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH VOLTAGE LIMITER HAVING DIFFERENT OUTPUT RANGES FORM NORMAL OPERATION AND PERFORMING OF AGING TEST

Group:

2306

RECEIVED

Examiner:

S. Baker

AUG 17 1995

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98

GROUP 2300

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

August 14, 1995

Sir:

In the matter of the above-identified application, applicants are submitting herewith copies of Form PTO-892 from related applications listing various references made of record in the related applications. Copies of the references can be found in the files of the related applications and apparently have already been considered by the Examiner.

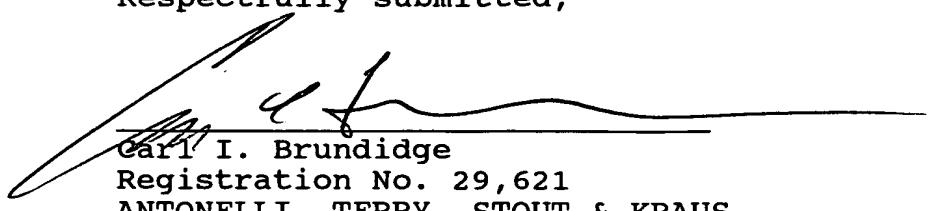
It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees,

to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case No. 501.20699VC3) please credit any excess fees to such deposit account.

Respectfully submitted,

(703) 312-6600
CIB/hpg


Carl I. Brundidge
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ANTONELLI, TERRY, STOUT & KRAUS